

FIGURE 1.

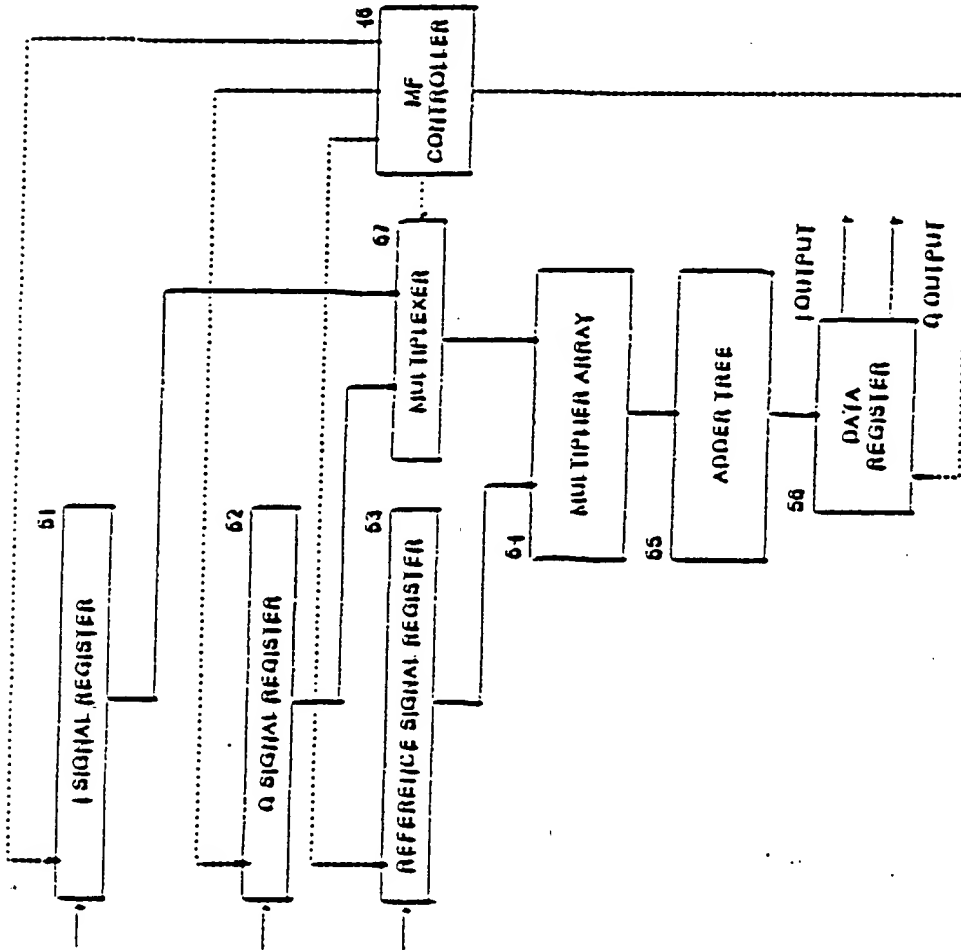


FIGURE 2

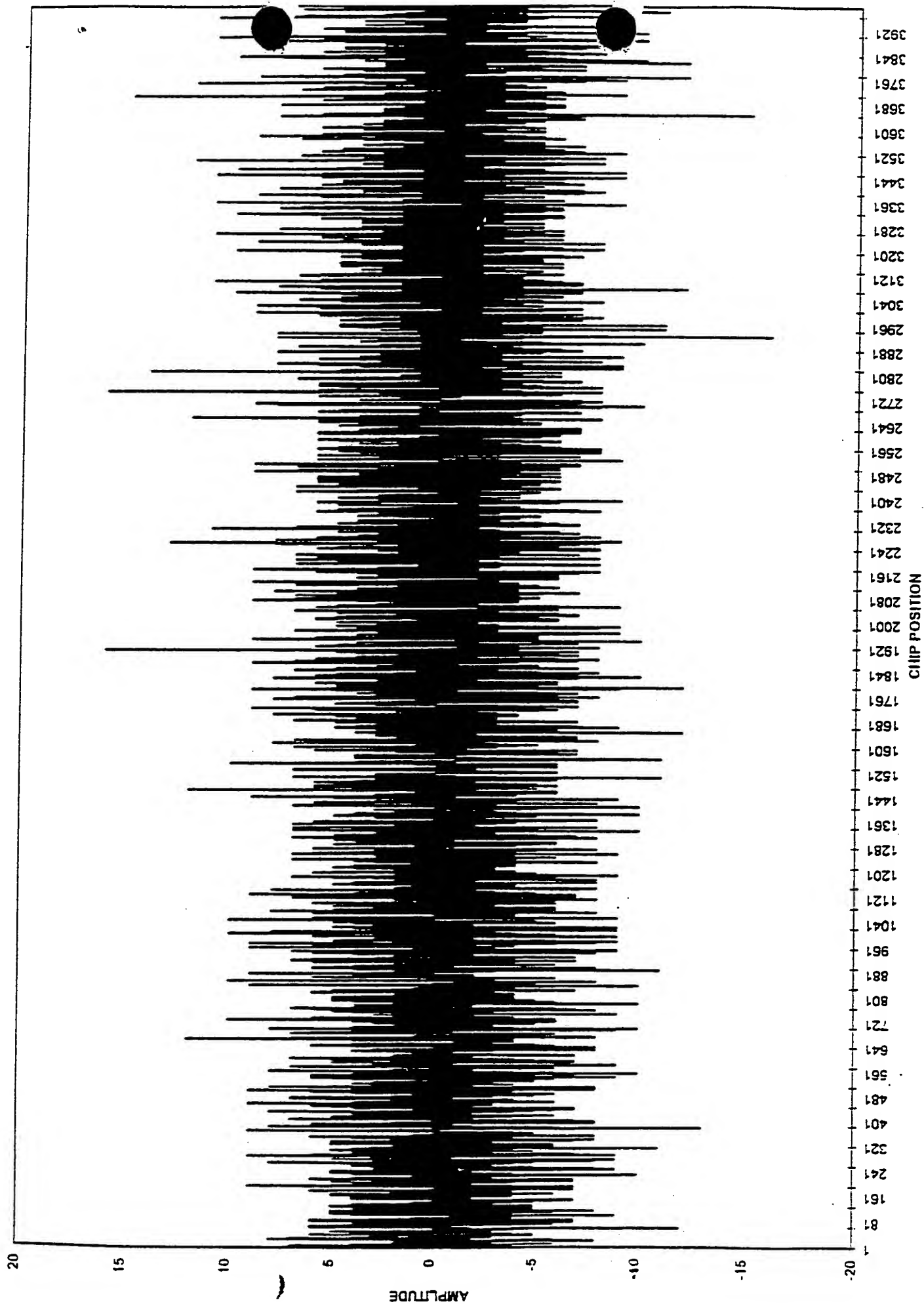


FIG. 3

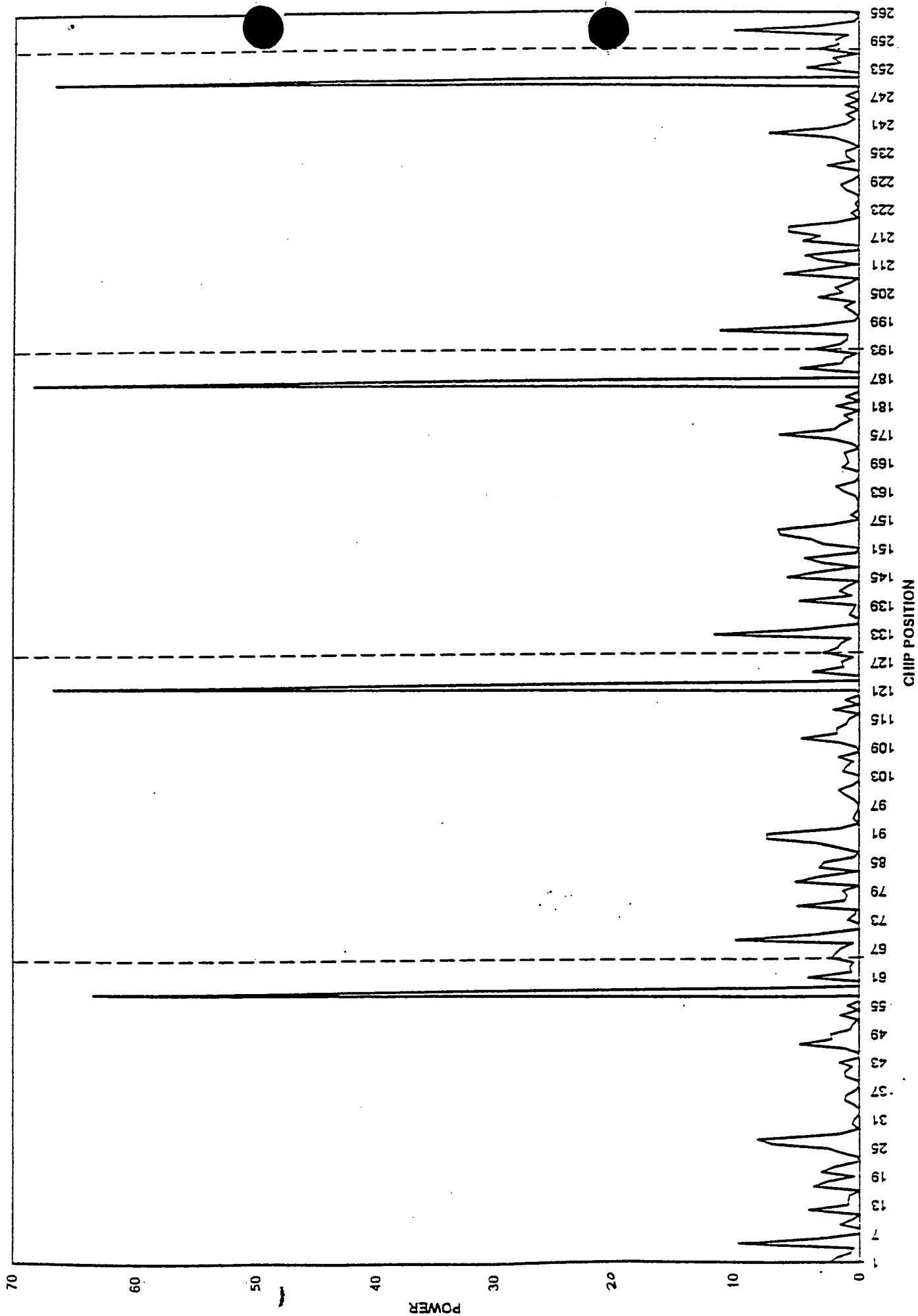


FIG. 4

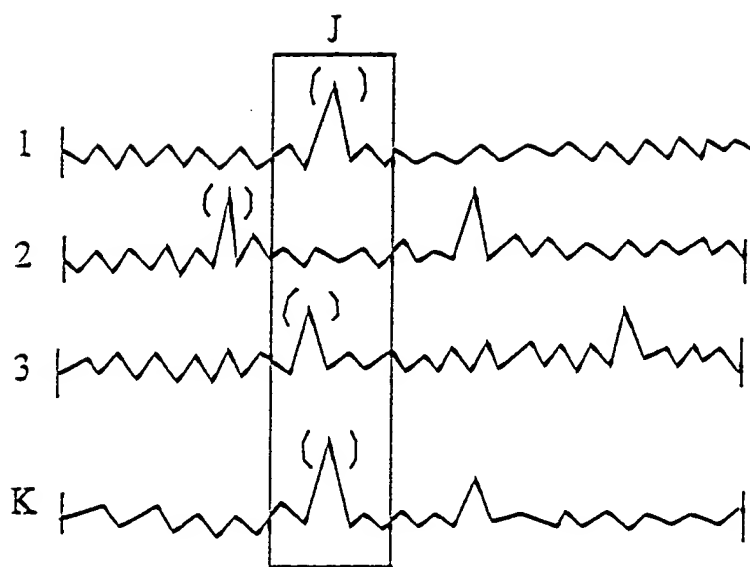


FIG. 5

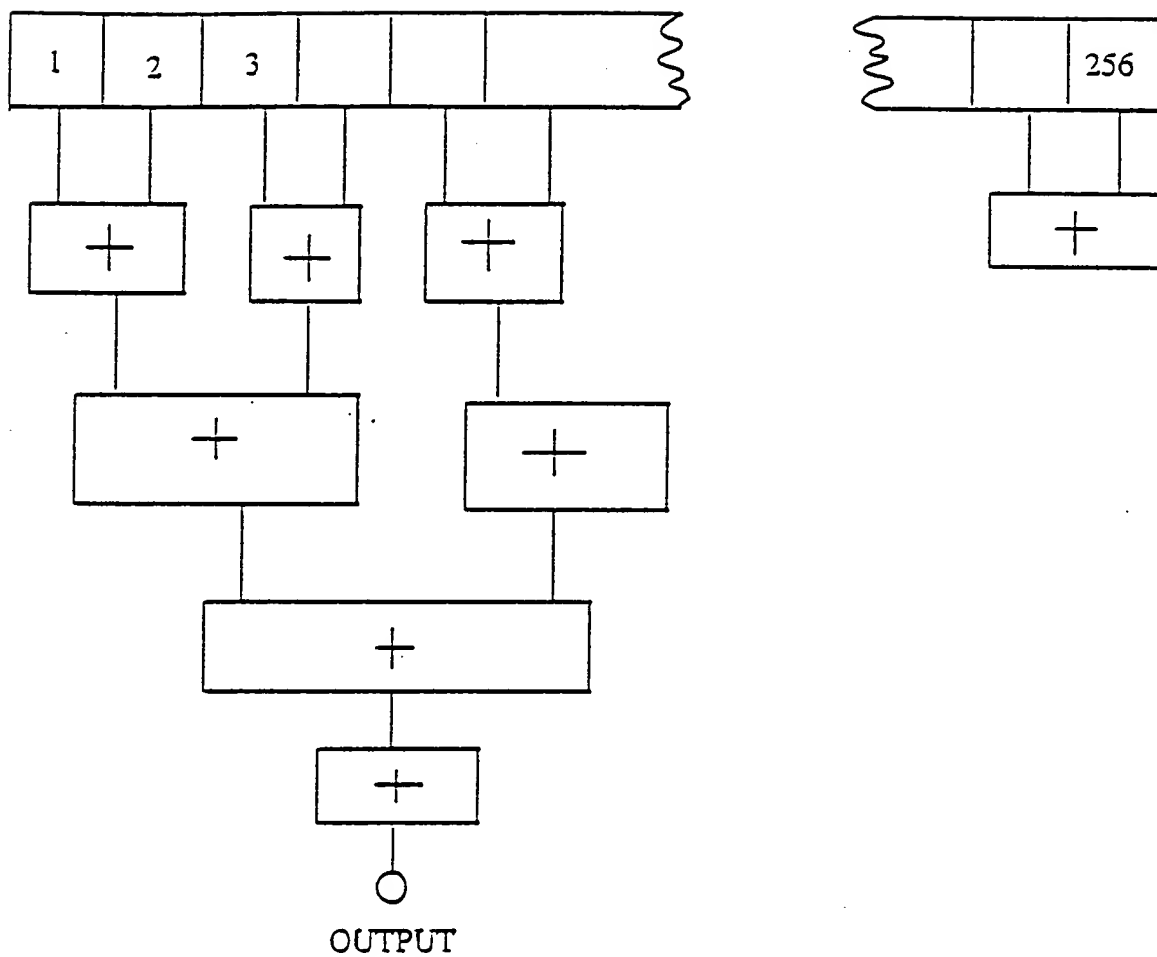


FIG. 6

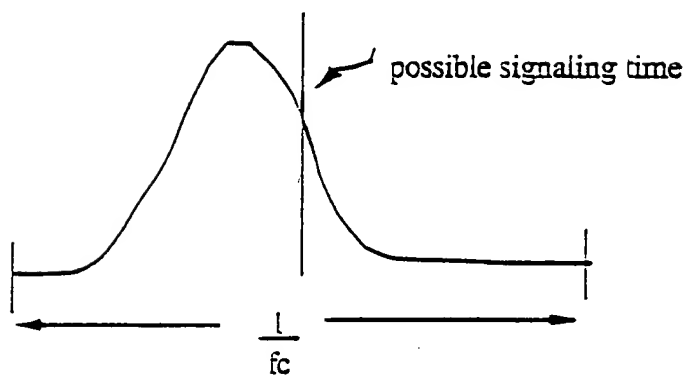


FIG. 7

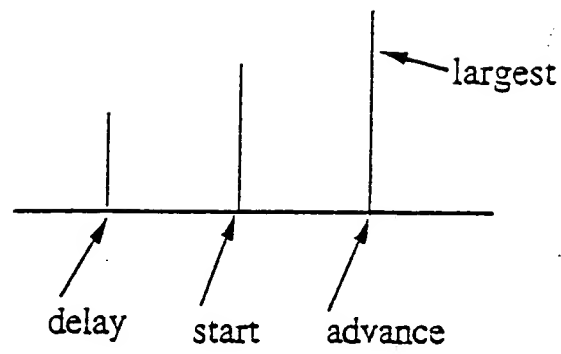


FIG. 8

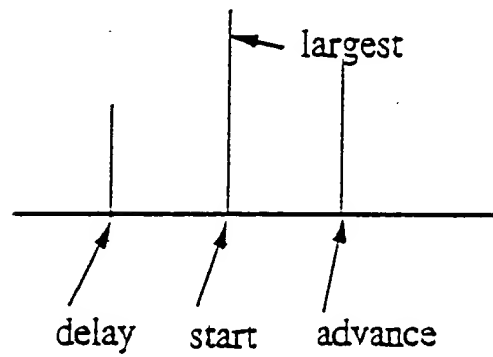


FIG. 9

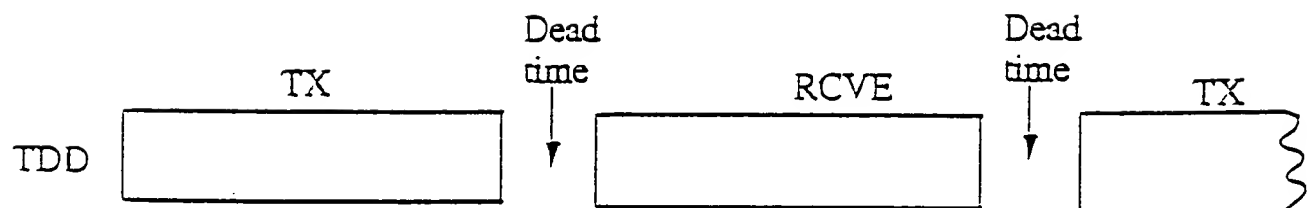


FIG. 10

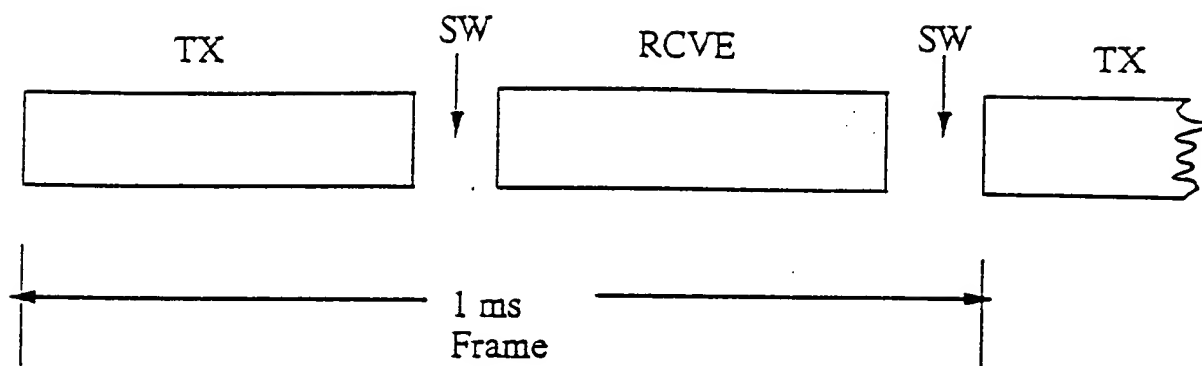


FIG. 11

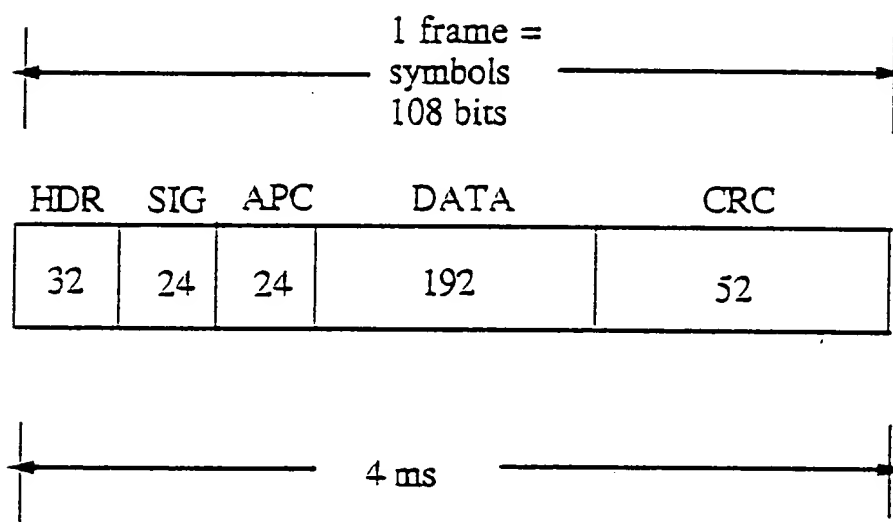


FIG. 12

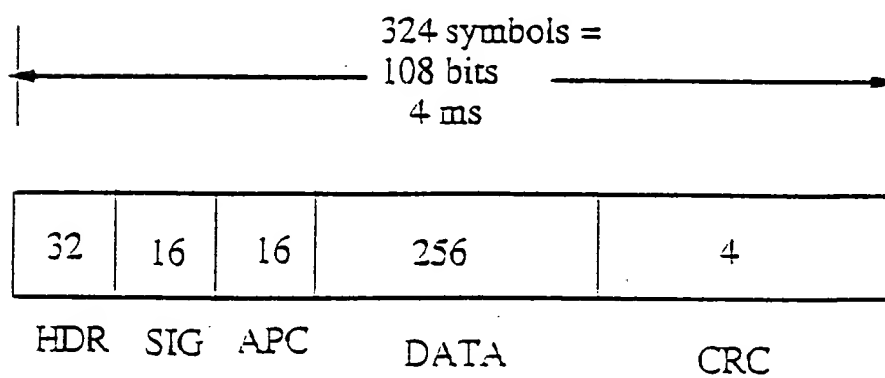


FIG. 13



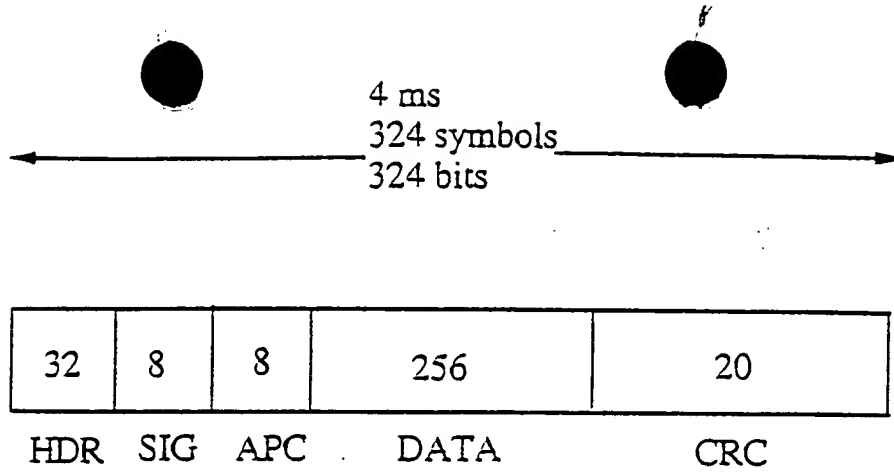


FIG. 14

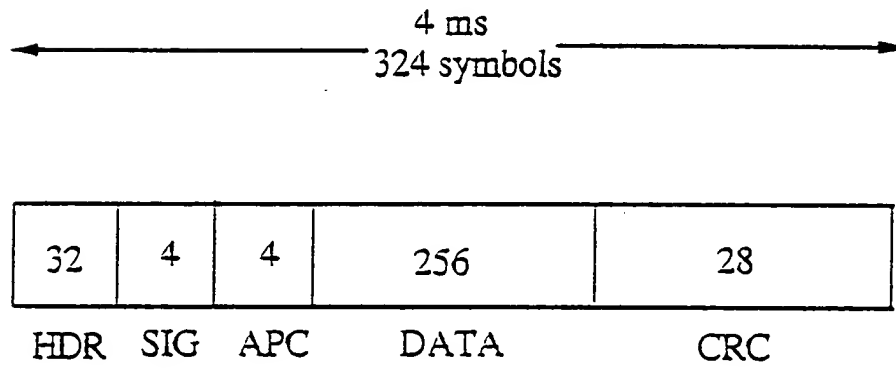


FIG. 15

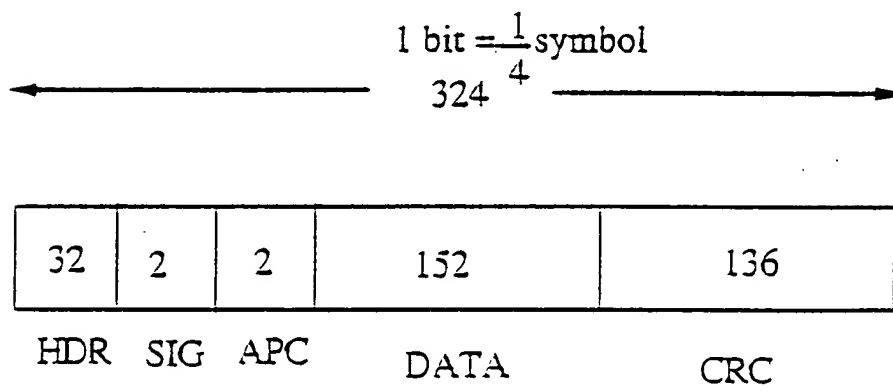


FIG. 16

$$1 \text{ bit} = \frac{1}{8} \text{ symbol}$$

32	1	1	152	138
HDR	SIG	APC	DATA	CRC

FIG. 17

$$1 \text{ bit} = \frac{1}{8} \text{ symbol}$$

32	1	1	192	98
HDR	SIG	APC	DATA	CRC

FIG. 18

$$1 \text{ bit} = \frac{1}{16} \text{ symbol}$$

32	$\frac{1}{2}$	$\frac{1}{2}$	192	99
HDR	SIG	APC	DATA	CRC

FIG. 19

$$1 \text{ bit} = \frac{1}{32} \text{ symbol}$$

← 324 →

32	$\frac{1}{4}$	$\frac{1}{4}$	256	35.5
HDR	SIG	APC	DATA	CRC

FIG. 20

1 symbol / ms  
1 symbol = 256 chips

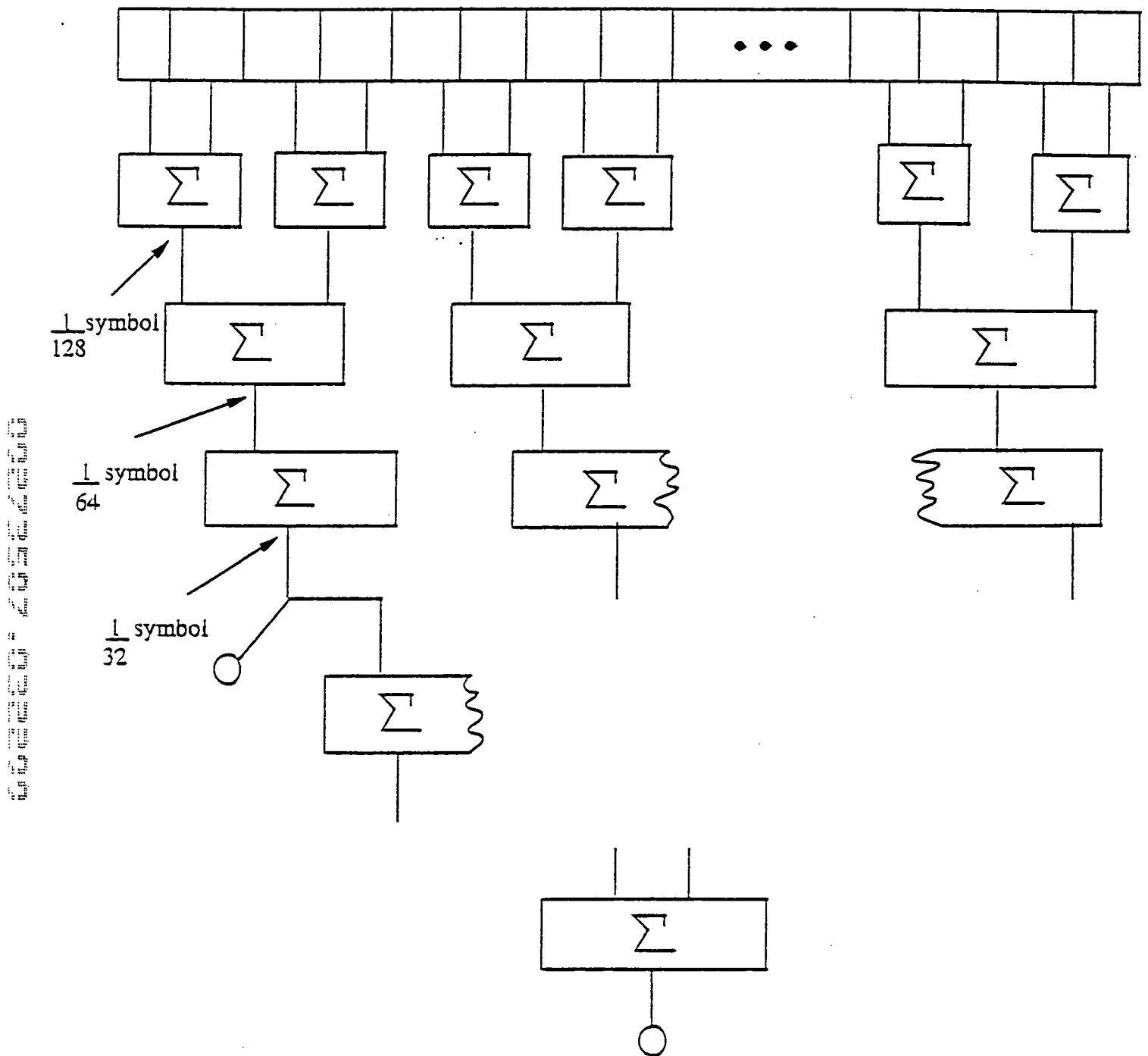


FIG. 21

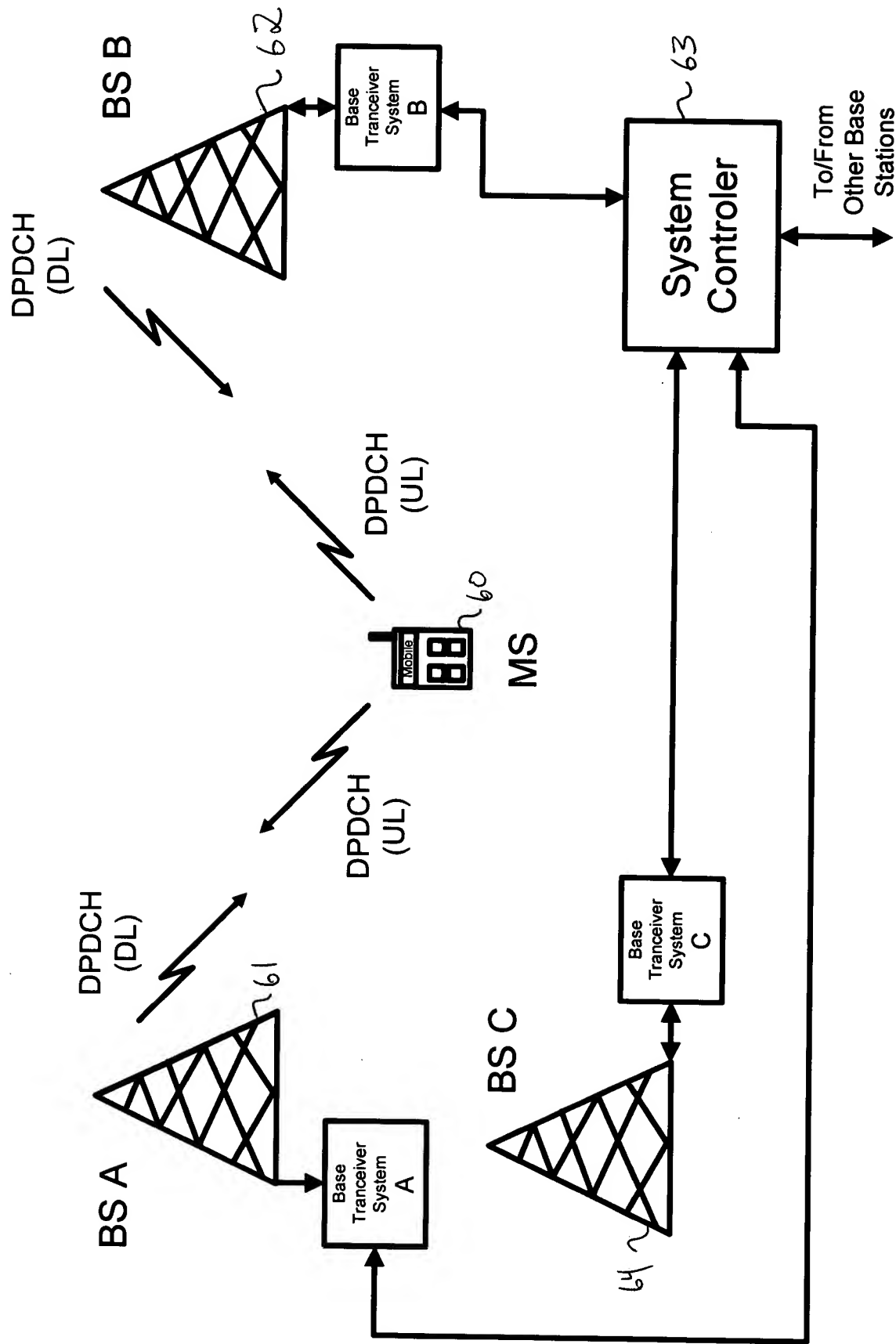


FIG. 22



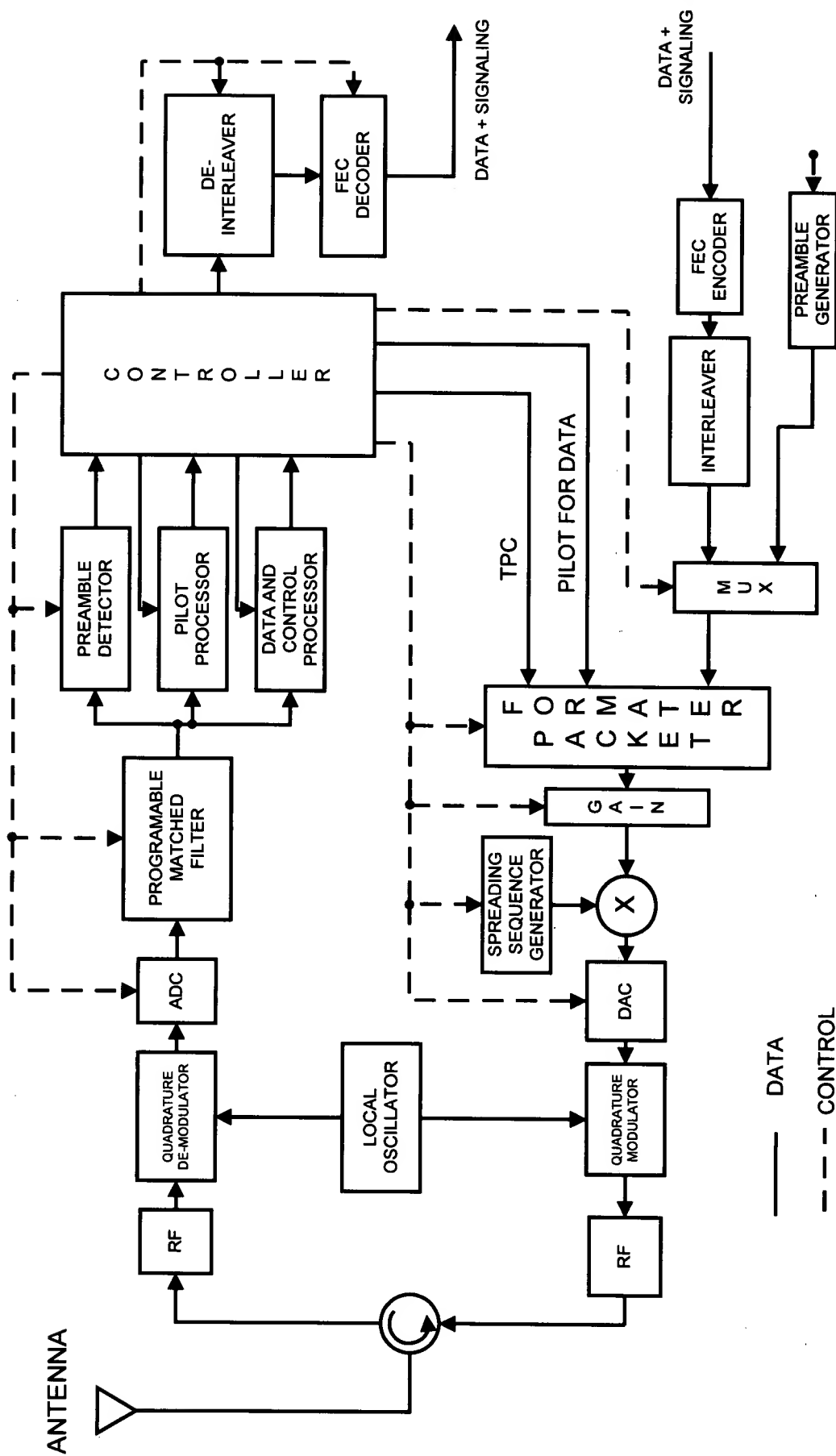


FIG. 24

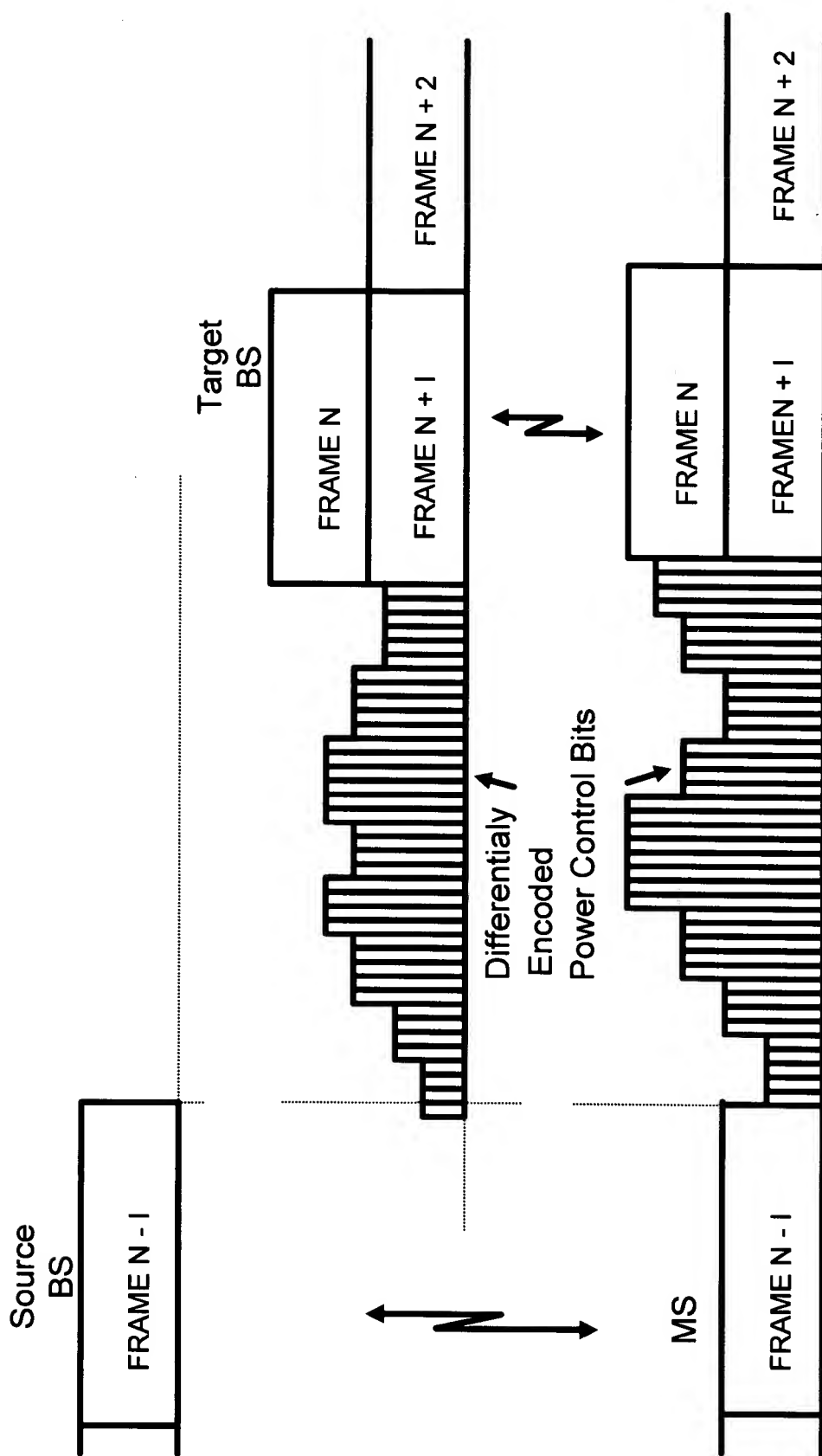
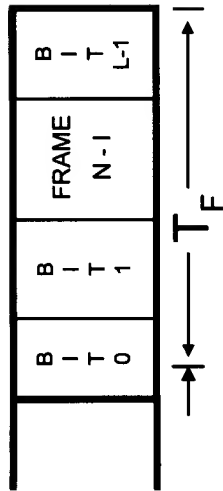


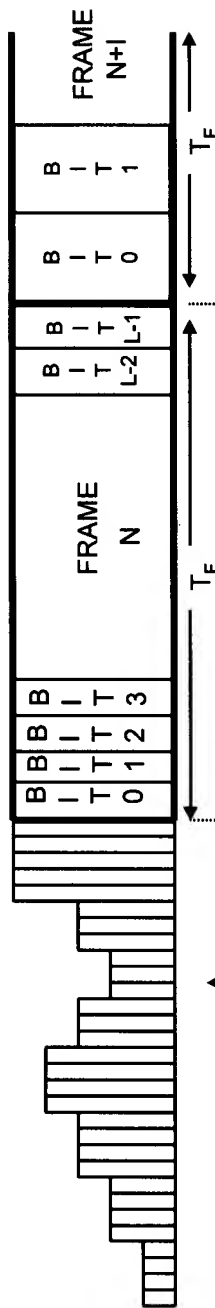
FIG 25



# Source BS

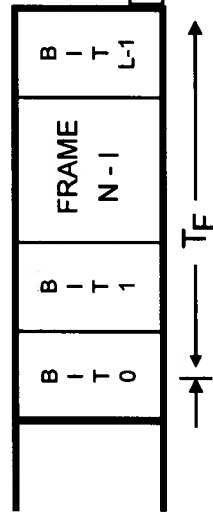


# Target BS



Differentially Encoded  
Power Control Bits

# MS



FIG

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